



APSEMI

**AC2M0045170D**  
 Silicon Carbide Power MOSFET  
 N-Channel Enhancement Mode

## Features

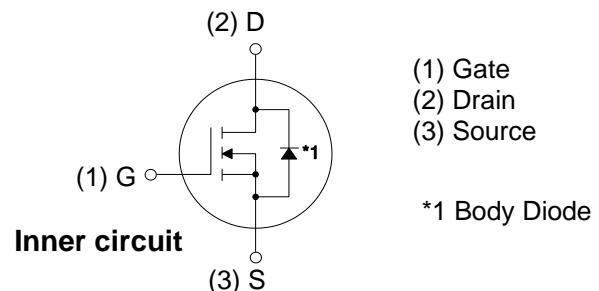
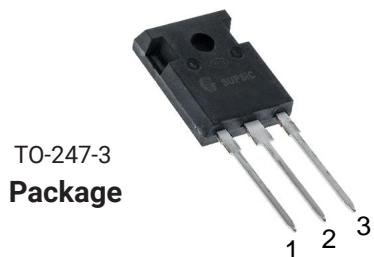
- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating ; RoHS compliant

Parameter	Rating	Units
$V_{DS}$	<b>1700</b>	V
$I_D @ 25^\circ C$	<b>78</b>	A
$R_{DS(on)}$	<b>45</b>	$\text{m } \Omega$



## Applications

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating



## Maximum Ratings ( $T_c = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
$V_{DS\max}$	Drain - Source Voltage	1700	V	$V_{GS} = 0 \text{ V}$ , $I_D = 100 \mu\text{A}$	
$V_{GS\max}$	Gate - Source Voltage	-10/+25	V	Absolute maximum values, AC ( $f > 1 \text{ Hz}$ )	
$V_{GSop}$	Gate - Source Voltage	-5/+20	V	Recommended operational values	
$I_D$	Continuous Drain Current	78	A	$V_{GS} = 20 \text{ V}$ , $T_c = 25^\circ \text{C}$	
		48		$V_{GS} = 20 \text{ V}$ , $T_c = 100^\circ \text{C}$	
$I_{D(\text{pulse})}$	Pulsed Drain Current	160	A	Pulse width $t_p$ limited by $T_{j\max}$	
$P_D$	Power Dissipation	528	W	$T_c = 25^\circ \text{C}$ , $T_j = 150^\circ \text{C}$	
$T_j$ , $T_{stg}$	Operating Junction and Storage Temperature	-40 to +150	°C		
$T_L$	Solder Temperature	260	°C	1.6mm (0.063") from case for 10s	
$M_d$	Mounting Torque	1 8.8	Nm lbf-in	M3 or 6-32 screw	



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Silicon Carbide Power MOSFET  
N-Channel Enhancement ModeElectrical Characteristics ( $T_c = 25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	1700			V	$V_{\text{GS}} = 0 \text{ V}, I_D = 100 \mu\text{A}$	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	2.0	2.6	4	V	$V_{\text{DS}} = V_{\text{GS}}, I_D = 18 \text{ mA}$	
			1.8		V	$V_{\text{DS}} = V_{\text{GS}}, I_D = 18 \text{ mA}, T_J = 150^\circ\text{C}$	
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current		2	100	$\mu\text{A}$	$V_{\text{DS}} = 1700 \text{ V}, V_{\text{GS}} = 0 \text{ V}$	
$I_{\text{GSS}}$	Gate-Source Leakage Current			600	nA	$V_{\text{GS}} = 20 \text{ V}, V_{\text{DS}} = 0 \text{ V}$	
$R_{\text{DS}(\text{on})}$	Drain-Source On-State Resistance		45	70	$\text{m}\Omega$	$V_{\text{GS}} = 20 \text{ V}, I_D = 50 \text{ A}$	
			90			$V_{\text{GS}} = 20 \text{ V}, I_D = 50 \text{ A}, T_J = 150^\circ\text{C}$	
$g_{\text{fs}}$	Transconductance		22		S	$V_{\text{DS}} = 20 \text{ V}, I_{\text{DS}} = 50 \text{ A}$	
			24.4			$V_{\text{DS}} = 20 \text{ V}, I_{\text{DS}} = 50 \text{ A}, T_J = 150^\circ\text{C}$	
$C_{\text{iss}}$	Input Capacitance		3617		pF	$V_{\text{GS}} = 0 \text{ V}$	
$C_{\text{oss}}$	Output Capacitance		174			$V_{\text{DS}} = 1200 \text{ V}$	
$C_{\text{rss}}$	Reverse Transfer Capacitance		6.7			$f = 1 \text{ MHz}$	
$E_{\text{oss}}$	$C_{\text{oss}}$ Stored Energy		105			$V_{\text{AC}} = 25 \text{ mV}$	
$E_{\text{ON}}$	Turn-On Switching Energy (SiC Diode FWD)		2.1		mJ	$V_{\text{DS}} = 1200 \text{ V}, V_{\text{GS}} = -5/20 \text{ V},$	
$E_{\text{OFF}}$	Turn Off Switching Energy (SiC Diode FWD)		0.86			$I_D = 50 \text{ A}, R_{\text{G(ext)}} = 2.5 \Omega, L = 105 \mu\text{H}, T_J = 150^\circ\text{C}$ , using SiC Diode as FWD	
$E_{\text{ON}}$	Turn-On Switching Energy (Body Diode FWD)		4.7		mJ	$V_{\text{DS}} = 1200 \text{ V}, V_{\text{GS}} = -5/20 \text{ V},$	
$E_{\text{OFF}}$	Turn Off Switching Energy (Body Diode FWD)		0.93			$I_D = 50 \text{ A}, R_{\text{G(ext)}} = 2.5 \Omega, L = 105 \mu\text{H}, T_J = 150^\circ\text{C}$ , using MOSFET as FWD	
$t_{\text{d(on)}}$	Turn-On Delay Time		65		ns	$V_{\text{DD}} = 1200 \text{ V}, V_{\text{GS}} = -5/20 \text{ V}$	
$t_r$	Rise Time		22			$I_D = 50 \text{ A}, R_{\text{G(ext)}} = 2.5 \Omega$ , Timing relative to $V_{\text{DS}}$	
$t_{\text{d(off)}}$	Turn-Off Delay Time		48			Inductive load	
$t_f$	Fall Time		18				
$R_{\text{G(int)}}$	Internal Gate Resistance		1.3		$\Omega$	$f = 1 \text{ MHz}, V_{\text{AC}} = 25 \text{ mV}$	
$Q_{\text{gs}}$	Gate to Source Charge		44		nC	$V_{\text{DS}} = 1200 \text{ V}, V_{\text{GS}} = -5/20 \text{ V}$	
$Q_{\text{gd}}$	Gate to Drain Charge		57			$I_D = 50 \text{ A}$	
$Q_g$	Total Gate Charge		186			Per IEC60747-8-4 pg 21	

## Reverse Diode Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$V_{\text{SD}}$	Diode Forward Voltage	4.1		V	$V_{\text{GS}} = -5 \text{ V}, I_{\text{SD}} = 25 \text{ A}$	
		3.6		V	$V_{\text{GS}} = -5 \text{ V}, I_{\text{SD}} = 25 \text{ A}, T_J = 150^\circ\text{C}$	
$I_s$	Continuous Diode Forward Current		72	A	$T_c = 25^\circ\text{C}, V_{\text{GS}} = -5 \text{ V}$	
$t_{\text{rr}}$	Reverse Recovery Time	70		ns	$V_{\text{GS}} = -5 \text{ V}, I_{\text{SD}} = 50 \text{ A}, V_R = 1200 \text{ V}$ $dif/dt = 1400 \text{ A}/\mu\text{s}$	
$Q_{\text{rr}}$	Reverse Recovery Charge	531		nC		
$I_{\text{rrm}}$	Peak Reverse Recovery Current	14		A		

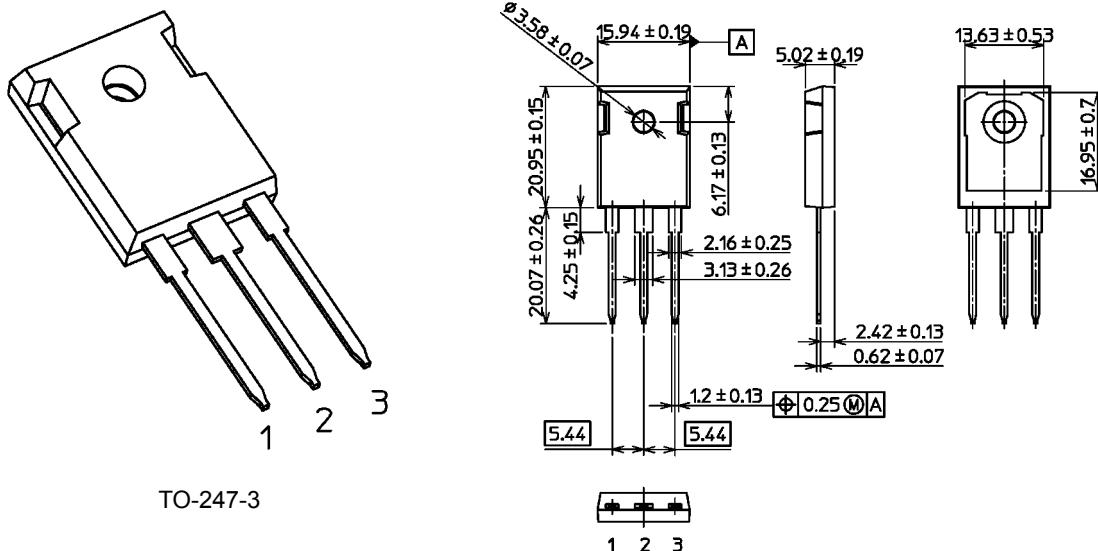
Note (1): When using SiC Body Diode the maximum recommended  $V_{\text{GS}} = -5 \text{ V}$ 

## Thermal Characteristics

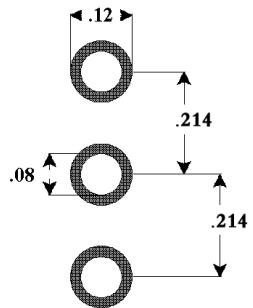
Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$R_{\text{θJC}}$	Thermal Resistance from Junction to Case	0.22	0.24	$^\circ\text{C}/\text{W}$		
$R_{\text{θJC}}$	Thermal Resistance from Junction to Ambient		40			

## Package Dimensions

Unit: mm



## **Recommended Solder Pad Layout**



TO-247-3