



APSEMI

AC2M1000170D

Silicon Carbide Power MOSFET
N-Channel Enhancement Mode

Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating ; RoHS compliant

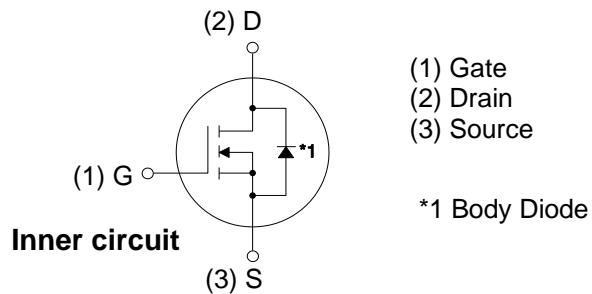
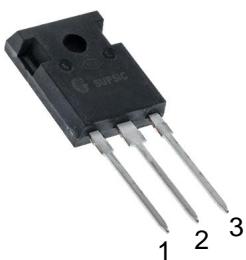
Parameter	Rating	Units
V_{DS}	1700	V
$I_D @ 25^\circ C$	6.0	A
$R_{DS(on)}$	1.0	m Ω

Applications



- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating

TO-247-3
Package



Maximum Ratings ($T_c = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{DSmax}	Drain - Source Voltage	1700	V	$V_{GS} = 0 V, I_D = 100 \mu A$	
V_{GSmax}	Gate - Source Voltage	-10/+25	V	Absolute maximum values	
V_{GSop}	Gate - Source Voltage	-5/+20	V	Recommended operational values	
I_D	Continuous Drain Current	6.0	A	$V_{GS} = 20 V, T_c = 25^\circ C$	
		3.3		$V_{GS} = 20 V, T_c = 100^\circ C$	
$I_{D(pulse)}$	Pulsed Drain Current	15	A	Pulse width t_p limited by T_{jmax}	
P_D	Power Dissipation	68	W	$T_c = 25^\circ C, T_j = 150^\circ C$	
T_J, T_{stg}	Operating Junction and Storage Temperature	-55 to +150	°C		
T_L	Solder Temperature	260	°C	1.6mm (0.063") from case for 10s	
M_d	Mounting Torque	1 8.8	Nm lbf-in	M3 or 6-32 screw	



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Silicon Carbide Power MOSFET
N-Channel Enhancement ModeElectrical Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	1700			V	$V_{\text{GS}} = 0 \text{ V}, I_D = 100 \mu\text{A}$	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	2.0	2.8	4	V	$V_{\text{DS}} = V_{\text{GS}}, I_D = 0.5 \text{ mA}$	
			2.4		V	$V_{\text{DS}} = V_{\text{GS}}, I_D = 0.5 \text{ mA}, T_J = 150^\circ\text{C}$	
I_{DSS}	Zero Gate Voltage Drain Current		1	100	μA	$V_{\text{DS}} = 1.7 \text{ kV}, V_{\text{GS}} = 0 \text{ V}$	
I_{GSS}	Gate-Source Leakage Current			250	nA	$V_{\text{GS}} = 20 \text{ V}, V_{\text{DS}} = 0 \text{ V}$	
$R_{\text{DS}(\text{on})}$	Drain-Source On-State Resistance		0.80	1.4	Ω	$V_{\text{GS}} = 20 \text{ V}, I_D = 2 \text{ A}$	
			1.4			$V_{\text{GS}} = 20 \text{ V}, I_D = 2 \text{ A}, T_J = 150^\circ\text{C}$	
g_{fs}	Transconductance		1.05		S	$V_{\text{DS}} = 20 \text{ V}, I_{\text{DS}} = 2 \text{ A}$	
			1.09			$V_{\text{DS}} = 20 \text{ V}, I_{\text{DS}} = 2 \text{ A}, T_J = 150^\circ\text{C}$	
C_{iss}	Input Capacitance		162		pF	$V_{\text{GS}} = 0 \text{ V}$	
C_{oss}	Output Capacitance		19			$V_{\text{DS}} = 1200 \text{ V}$	
C_{rss}	Reverse Transfer Capacitance		2.2			$f = 1 \text{ MHz}$	
E_{oss}	C_{oss} Stored Energy		10.2			$V_{\text{AC}} = 25 \text{ mV}$	
E_{ON}	Turn-On Switching Energy		88		μJ	$V_{\text{DS}} = 1.2 \text{ kV}, V_{\text{GS}} = -5/20 \text{ V}$	
E_{OFF}	Turn Off Switching Energy		14			$I_D = 2 \text{ A}, R_{\text{G(ext)}} = 2.5 \Omega, L = 1478 \mu\text{H}, T_J = 150^\circ\text{C}$	
$t_{\text{d(on)}}$	Turn-On Delay Time		5				
t_r	Rise Time		19		ns	$V_{\text{DD}} = 1.2 \text{ kV}, V_{\text{GS}} = -5/20 \text{ V}$	
$t_{\text{d(off)}}$	Turn-Off Delay Time		14			$I_D = 2 \text{ A}, R_{\text{G(ext)}} = 2.5 \Omega, R_L = 600 \Omega$	
t_f	Fall Time		64			Timing relative to V_{DS}	
$R_{\text{G(int)}}$	Internal Gate Resistance		24.8			Per IEC60747-8-4 pg 83	
Q_{gs}	Gate to Source Charge		4		nC	$V_{\text{DS}} = 1.2 \text{ kV}, V_{\text{GS}} = -5/20 \text{ V}$	
Q_{gd}	Gate to Drain Charge		12			$I_D = 2 \text{ A}$	
Q_g	Total Gate Charge		20			Per IEC60747-8-4 pg 21	

Reverse Diode Characteristics

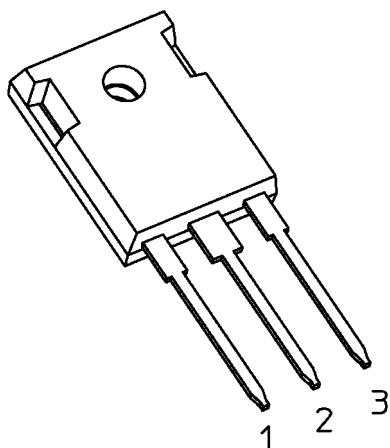
Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V_{SD}	Diode Forward Voltage	3.8		V	$V_{\text{GS}} = -5 \text{ V}, I_{\text{SD}} = 1 \text{ A}, T_J = 25^\circ\text{C}$	
		3.3		V	$V_{\text{GS}} = -5 \text{ V}, I_{\text{SD}} = 1 \text{ A}, T_J = 150^\circ\text{C}$	
I_s	Continuous Diode Forward Current		4	A	$T_c = 25^\circ\text{C}$	
t_{rr}	Reverse Recovery Time	33		ns	$V_{\text{GS}} = -5 \text{ V}, I_{\text{SD}} = 2 \text{ A}, T_J = 150^\circ\text{C}$	
Q_{rr}	Reverse Recovery Charge	31		nC	$V_R = 1.2 \text{ kV}$ $dif/dt = 1135 \text{ A}/\mu\text{s}$	
I_{rrm}	Peak Reverse Recovery Current	3		A		

Note (1): When using SiC Body Diode the maximum recommended $V_{\text{GS}} = -5 \text{ V}$

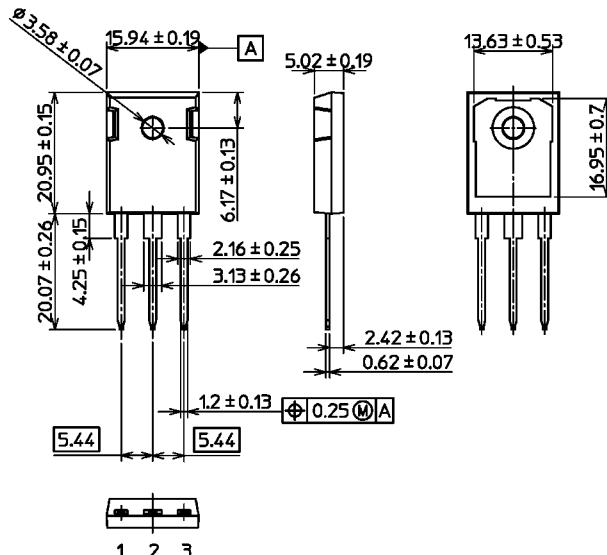
Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$R_{\text{t}_{\text{JC}}}$	Thermal Resistance from Junction to Case	1.7	1.8	°C/W		
$R_{\text{t}_{\text{JA}}}$	Thermal Resistance from Junction to Ambient		40			

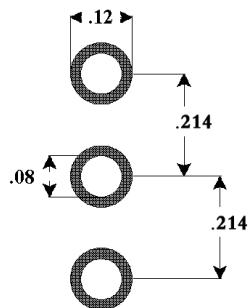
Package Dimensions



TO-247-3



Recommended Solder Pad Layout



TO-247-3